

Form PTO-1449		U.S. Department of Commerce Patent and Trademark Office		Attorney Docket No. 9219-2	Serial No. To Be Assigned		
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				Applicants: Martin T. Budrovic, David J. Kolson			
				Filing Date: Concurrently Herewith		GAU: <i>2183</i>	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initials		Document No.	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>ES</i>	1	5,991,452	11/23/99	Shimizu et al.	382	248	
<i>ES</i>	2	5901,310	05/04/99	Rahman et al.	395	651	
<i>ES</i>	3	5,764,994	06/09/98	Craft	395	709	
<i>ES</i>	4	5,410,671	04/25/95	Elgamal et al.	395	425	<i>1c841 U.S. PTO 09/13/00 736682</i>
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<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Subclass	Translation Yes / No
	14						
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<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
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Applicants:  
Budrovic et al.

Filing Date: December 13, 2000      GAU: 2185

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

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2	Lekatsas et al., <i>Arithmetic Coding for Low Power Embedded System Design</i> , IEEE, Proceedings of the Data Compression Conference 2000, pp. 430-439
3	Lefurgy et al., <i>Evaluation of a High Performance Code Compression Method</i> , Proceedings of Micro-32, November 16-18, 1999, pp. 93-102
4	Benini et al., <i>Region Compression: A New Scheme for Memory Energy Minimization in Embedded Systems</i> , Proceedings of the 25 <sup>th</sup> EUROMICRO Conference, 1999, pp. 311-317
5	Larin et al., <i>Compiler-Driven Cached Code Compression Schemes for Embedded ILP Processors</i> , Proceedings of the 32 <sup>nd</sup> Annual ACM/IEEE International Symposium on Microarchitecture, 1999, pp. 82-91
6	Lekatsas et al., <i>SAMC: A Code Compression Algorithm for Embedded Processors</i> , IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 18, No. 12, December 1999, pp. 1689-1701
7	Liao et al., <i>Code Density Optimization for Embedded DSP Processors Using Data Compression Techniques</i> , IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 17, No. 7, July 1998, pp. 601-609
8	Araujo et al., <i>Code Compression Based on Operand Factorization</i> , Proceedings of the 31 <sup>st</sup> Annual ACM/IEEE International Symposium on Microarchitecture, 1998, pp. 194-201
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